Abstract

Digital Code Division Multiple Access (CDMA) scheme allows many users to transmit and receive at the same time using a single channel. CDMA techniques can use synchronous or asynchronous mechanisms. The work in this paper uses the less studied asynchronous mechanism. The transmitter and receiver are synchronized with the help of a delay element. The simulation and synthesis carried out using VHDL tool shows minimum power consumption and an increase in the overall speed of the system.

References

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**Index Terms**

Computer Science            Mobile Communication

**Keywords**

Cdma    Correlator    Spreading Sequence