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Abstract

Cascaded multilevel inverter (CMLI) is emerging as a new breed of power converter options for high-power applications. Various topologies and modulation strategies of this inverter has been proposed. In this paper, Matlab simulink of CMLI using Carrier Based Pulse Width Modulation (CBPWM) techniques is developed to get optimum design and performances. Some parameter of this Simulink model can be varied, so some interesting characteristics of the inverter can be optimized. A simulation result shows that the system is easy and modular to adjust, and optimum performances can improve.

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Index Terms

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Keywords

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